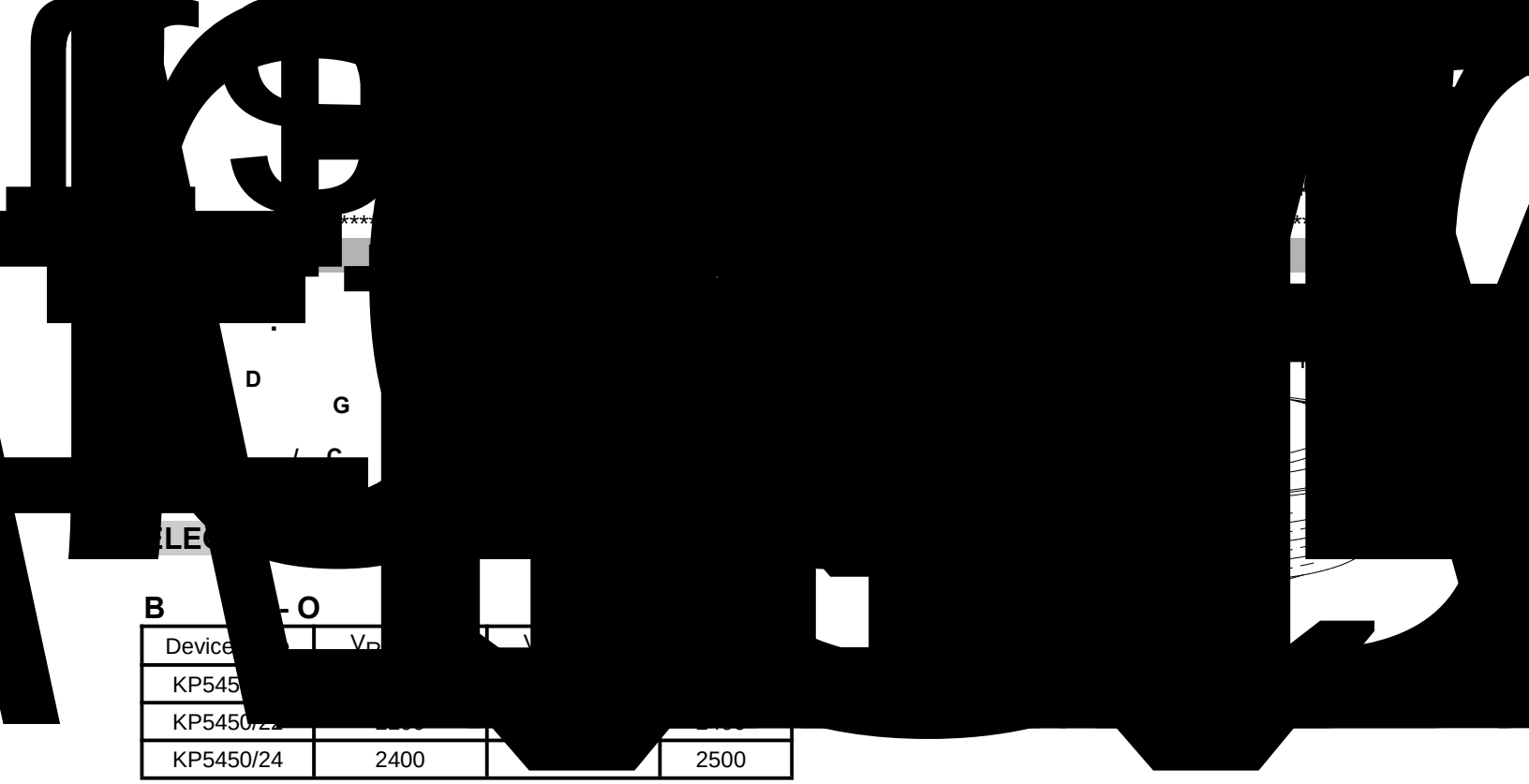


B - O

Device	V_{RRM}	V_{DRM}	V_{RSM}
KP545			
KP5450/24	2400		2500

V_{RRM} = Repetitive peak reverse voltage
V_{DRM} = Repetitive peak off state voltage
V_{RSM} = Non repetitive peak reverse voltage (2)

Repetitive peak reverse leakage and off state leakage	I_{RRM}/I_{DRM}	10 mA 200 mA (3)
Critical rate of voltage rise	dv/dt (4)	1000 V/μs

C - O

G

P		M .	M .	.		C
Peak gate power dissipation	P_{GM}		20		W	
Average gate power dissipation	$P_{G(AV)}$		4		W	
Gate-trigger current	I_{GT}		250		mA	V_D =12 V;R_L = 3 ohms;T_j = +25 °C
Gate- trigger voltage	V_{GT}	0.7	3.0		V	V_D = 12 V;R_L = 3 ohms;T_j = +25 °C
Peak negative voltage	V_{GRM}		5		V	

D

P		M .	M .	.		C
Delay time	t_d		3.0	2.5	μs	I_{TM}=100A; V_D=67%V_{DRM} Gate pulse:V_G=30V; R_G=10ohms; t_r=0.1μs; t_p=20μs
Turn-off time (with V_R = -5 V)	t_q			450	μs	I_{TM} =2000A; di/dt =-10A/μs; V_R =100V; dv/dt=30V/μs ; V_D=67%V_{DRM};Tj=125
Reverse recovery charge	Q_{rr}				μC	I_{TM}=2000A di/dt=-10A/μs; V_R=100V; T_j=125

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P		M .	M .	.		C
Operating temperature	T_j	-40	+125		°C	
Storage temperature	T_{stg}	-40	+140		°C	
Thermal resistance - junction to case	$R_{\Theta(j-c)}$		0.006		°C/W	Double sided cooled
Thermal resistance - case to heatsink	$R_{\Theta(c-s)}$		0.0015		°C/W	Double sided cooled
Mounting force	F	76	84	80	kN	
Weight	m			1.9	kg	

